

BoS Meeting - II

Date: 30/03/2019.

Head of the department convened the meeting of Board of studies members in board room at 10:00AM on 30th march 2018. to discuss and approve the proposed curriculum in ECE for III B.Tech. for the autonomous system commencing from the Academic year 2018-19.

Agenda for meeting

1. To welcome the BOS members.
2. To review the resolutions made in the last Bos meeting held on 8th may 2018.
3. To discuss, review and approve the proposed curriculum in ECE for III B.Tech. for autonomous system commenced from the Academic year 2018-19.
4. Any other matter with the permission of the chair.

Minutes of the meeting

Agenda I: To welcome the Bos members

Resolution I: Head of the department as the chair of the meeting, invited all the board of studies members.

Agenda II: To review resolutions made in the last Bos meeting held on 8th may 2018.

Resolution II: The members reviewed the resolutions made in the last Bos meeting held on 8th may 2018.

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Agenda: III: To discuss, review and approve the proposed curriculum in ECE for III. B-Tech for autonomous system commenced from the academic year 2018-19.

Resolution III: The chair presented the AICTE and proposed III. B-Tech curriculum. The chair has discussed about the enhancement and committee members suggested the following.

(a) suggested to remove VLSI Design Course (program elective -I) in VI semester and replace with CMOS VLSI Design in V semester in place of personality development and professional communication course.

(b) The members suggested that, in CMOS VLSI Design subject 20% on technology, 40% analog and 40% Digital VLSI part has to added.

(c) Move personality development and professional communication course to either professional or open elective course.

(d) move control system course from V semester to VII semester open elective -I or II.

(e) prepare Biology for engineers course syllabus by including telemedicine and BMT.

(f) Consider D.V. Hall book as main textbook for mpmc course.

(a) Include DSP processors in DSP course and refer the text-book S. Haykin.

(b) The members has suggested to change the representation of experiments name of DSP Lab such as Design of implementation.

(i) The members suggested to combine mWE and AWP courses and include laboratory also.

(j) It is suggested to include architecture way of dealing of ADSP course.

(k) The members are suggested to check the possibility adding extended topics related to applications in DIP.

(l) It is suggested to deal CN subject ~~through~~ ^{through} queuing theory approach.

Agenda IV: Any other matter with the permission of the chair

Resolution IV: The members are suggested not to repeat words such as analyze and design more than once in course objectives and outcomes. Remove more credits for term paper with seminar.

The chair has expressed gratitude to the all the BOS members.

